# **□ MN150222**

Туре	MN150222		
ROM (×8-Bit)	2 K		
RAM (×4-Bit)	96		
Number of Instructions	51		
Minimum Instruction Execution Time	1.0 μs at 1/8 frequency dividing (at 4.5 V to 5.5 V, 8 MHz) 4.0 μs at 1/8 frequency dividing (at 2.0 V to 5.5 V, 2 MHz)		
	8.0 µs at 1/8 frequency dividing (at 1.8 V to 5.5 V, 1 MHz)		

Interrupts

• Reset • External

Timer Counter

Timer Counter: 8-Bit × 1 (Event Count, Timer Output)

Clock Source

System Clock, 1/16384 of OSC Oscillation Clock, TCI Input

Interrupt Source

Overflow of Timer Counter

Time Base Counter: 1

Clock Source Interrupt Source 1/1 of OSC Oscillation Clock
Overflow of Time Base Counter

Watchdog Timer (Mask Option)

	Time Base Output			
fosc = 8 MHz	2 kHz	4 kHz	8 kHz	16 kHz
fosc = 1 MHz	0 25 kHz	0 5 kHz	1 kHz	2 kHz

1/0 Pins 1/0

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- Common use 7 Specified pull-up Resistor available 11 (Mask Option)
- Specified output architecture available Nch Open drain / Push-Pull 11 (Mask Option)
- 4ch LED direct drive available (20 mA / 2 0 V)

#### **Electrical Characteristics**

#### **Supply Current**

Parameter	Symbol	Condition	min	Limit typ	max	Unit
Operating Supply Current	IDD1	fosc = 8 MHz	ĺ	4 0	80	mA
	IDD2	fosc = 32 768 kHz		30	60	μА
Supply Current at HALT	IDD3	fosc = 32 768 kHz		15	30	μΑ
Supply Current at STOP	IDD4	illestics		0 5	5 0	μΑ
Auto reset current consumption	IDD5	110, 61,		30	80	μΑ

(Ta = -40 °C to +85 °C, VDD = 5.0 V, VSS = 0 V)

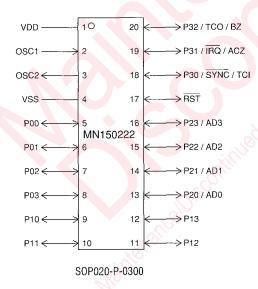
#### A/D Converter Characteristics

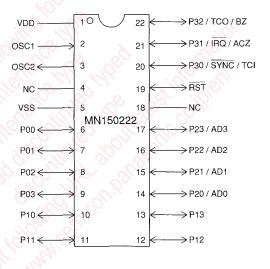
Parameter	Symbol	Condition	888	Limit typ	max	Unit
A/D Conversion Relative Error	,				±3	LSB
A/D Conversion Time		fosc = 8 MHz		15	27	μs
Analog Input Voltage	VIA		VSS		VDD	٧

(Ta = -40 °C to +85 °C, VDD = 5.0 V, VSS = 0 V)

A/D Inputs	10-Bit × 4ch (with S/H)				
Zero-Cross Inputs	1				
Special Ports	Buzzer Output (1 kHz, 2 kHz, 4 kHz fosc = at 4 MHz)				
Notes	Auto-Reset circuit selectable (Mask option)				
Package	SOP020-P-0300, SDIP022-P-0300				
Support Tool					
In-Circuit Emulator	PX-ICE1500 + PX-PRB150222				
■ EPROM built-in Type	Туре	MN15P0222			
	ROM (× 8-Bit)	2 K			
	RAM (× 4-Bit)	96			
	Minimum Instruction Execution Time	1 0 μs (at 4 5 V to 5.5 V, 8 MHz) 4 0 μs (at 2 35 V to 5 5 V, 2 MHz)			
	Package	S0P020-P-0300, SDIP022-P-0300			

### Pin Assignment





SDIP022-P-0300

P00 to P03 High current output port NC Nothing connected with pin

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